

High-Performance Single-Ended Clock Buffer (CTB2110)

Description

California Triangle provides high-performance, low-noise single-ended clock buffers for communication, radar, measurement, and industrial control fields. This buffer can distribute 10 channels of ultra-low jitter clocks from single-ended, differential, or crystal inputs. It supports glitch-free pulse operation for synchronized output. Ultra-low skew, low noise, and high power supply rejection ratio (PSRR) make this buffer very suitable for various network interconnections, such as telecommunications, server, and storage area network interconnections, RRU Lo reference distribution, medical, and testing equipment applications. Additional jitter is a maximum of 30fs.

Product view



CTB2110 Front View



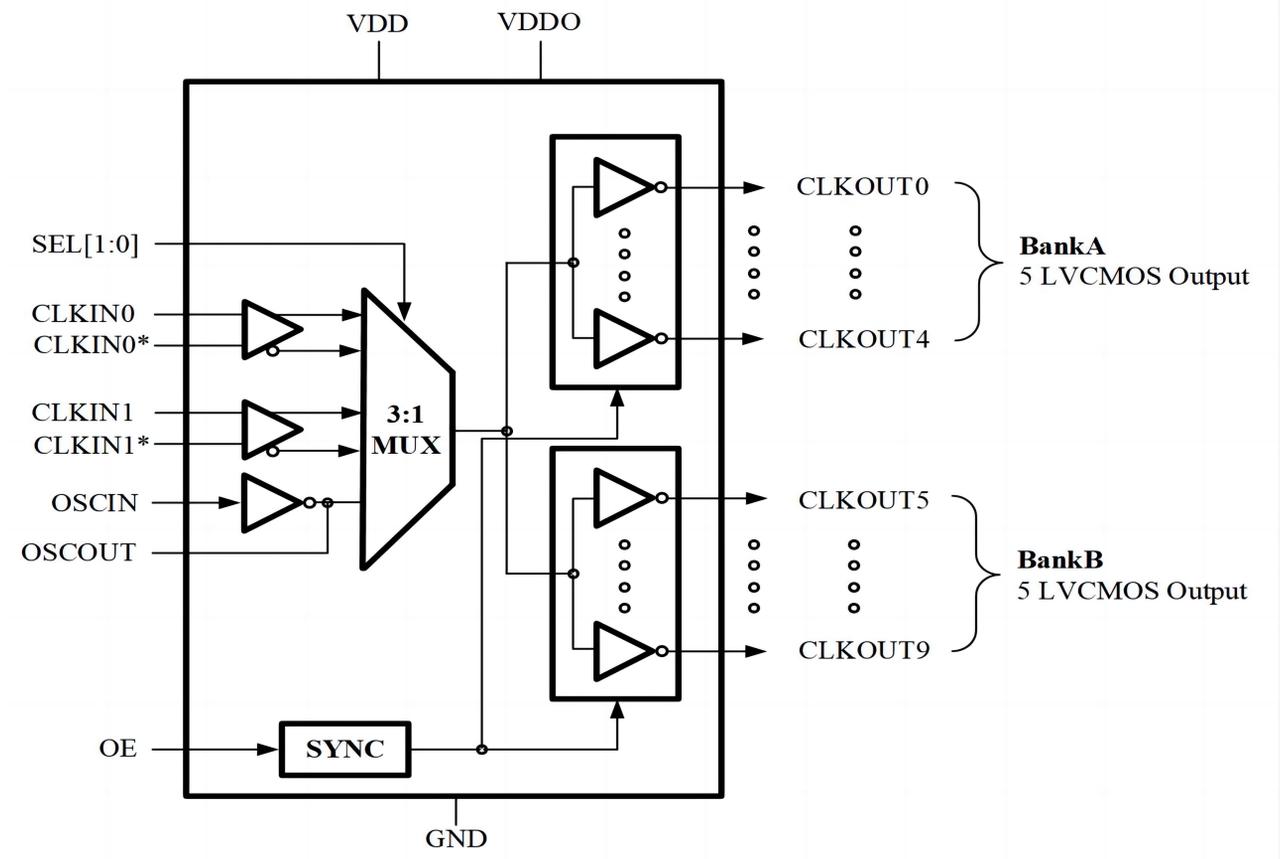
CTB2110 Rear View



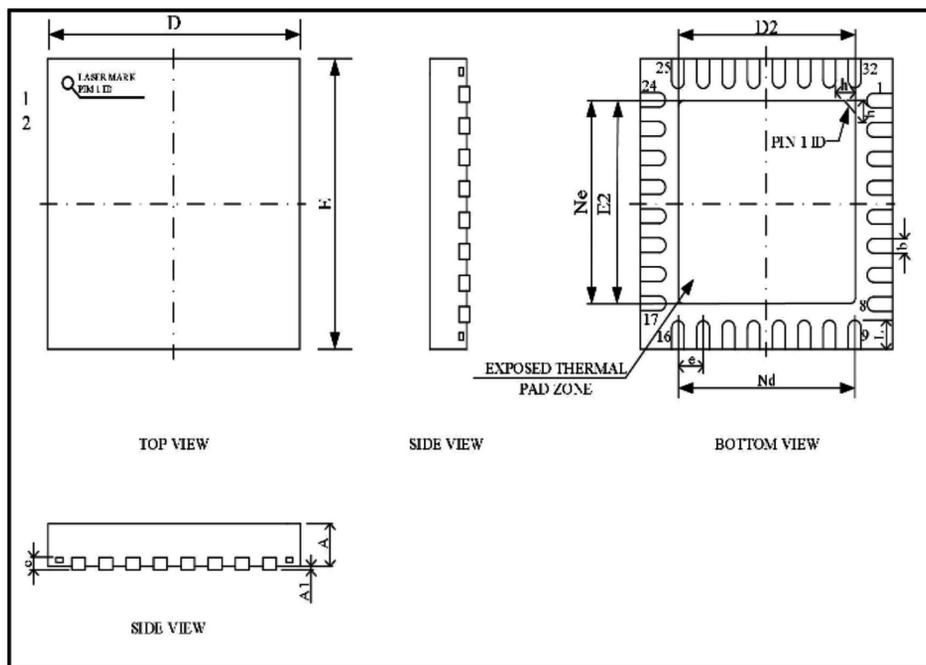
Key Performance Indicators

Model	Frequency Range	Input Level Type	Number of Input Ports	Core Voltage	Output Voltage	Number of Output Ports	Output Level Type	Temperature Range	Packaging
CTB2110	DC~200 MHz	LVPECL LVDS HCSI SSTL LVCMOS /LVTTTL Crystal Oscillator	3	3.3V	1.5V 1.8V 2.5V 3.3V	10	LVCMOS	-40~+85°C	QFN32

Functional Diagram



Packaging Diagram



Packaging Information

SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	0.65	0.75	0.85
A1	--	0.02	0.05
b	0.20	0.25	0.30
c	0.18	0.20	0.25
D	4.90	5.00	5.10
D2	3.40	3.50	3.60
e	0.50BSC		
Ne	3.50BSC		
Nd	3.50BSC		
E	4.90	5.00	5.10
E2	3.40	3.50	3.60
L	0.35	0.40	0.45
h	0.25	0.30	0.35